What is claimed is:

1. A method comprising:

passing data through a reconfigurable partial response encoder to create a spectral notch; and

modifying a characteristic of the reconfigurable partial response encoder to change a frequency characteristic of the spectral notch.

2. The method of claim 1 further comprising pre-coding the data prior to passing through the reconfigurable partial response encoder.

3. The method of claim 2 further comprising passing the data through a spectral whitening encoder.

4. The method of claim 1 wherein modifying a characteristic of the reconfigurable partial response encoder comprises modifying a clock frequency of the reconfigurable partial response encoder.

5. A method comprising:

detecting errors in a data stream received over a wireless link; and

modifying characteristics of a partial response encoder in a digital data port to reduce the errors.

6. The method of claim 5 wherein modifying characteristics comprises modifying a clock frequency.

7. The method of claim 5 wherein:

the wireless link operates in a frequency band; and

modifying characteristics of a partial response encoder comprises moving a spectral notch in frequency relative to the frequency band.

8. The method of claim 7 wherein modifying characteristics comprises modifying a clock frequency at which the partial response encoder operates.

9. An apparatus comprising a reconfigurable partial response encoder to encode data and create a spectral notch in the region of a wireless frequency band.

10. The apparatus of claim 9 wherein the spectral notch is between about 800 MHz and about 900 MHz.

11. The apparatus of claim 9 further comprising a low pass filter to reduce spectral energy in wireless frequency bands above the spectral notch.

12. The apparatus of claim 9 wherein the reconfigurable partial response encoder implements $1\text{-}D^4$.

13. The apparatus of claim 12 wherein the reconfigurable partial response encoder operates at a clock frequency of approximately 3.4 GHz.

14. The apparatus of claim 9 wherein the reconfigurable partial response encoder implements $1\text{-}D^2$.

15. The apparatus of claim 9 wherein the reconfigurable partial response encoder implements 1+D.

16. The apparatus of claim 9 wherein the wireless frequency band corresponds to global positioning system (GPS) signals.

17. The apparatus of claim 9 wherein the wireless frequency band corresponds to cellular phone signals.

18. The apparatus of claim 9 wherein the wireless frequency band corresponds to wireless local area network (WLAN) signals.

19. An apparatus comprising:

a wireless interface circuit; and

a digital interface circuit that includes a partial response encoder to create a spectral notch.

20. The apparatus of claim 19 wherein the spectral notch is near in frequency to a frequency of operation of the wireless interface circuit.

21. The apparatus of claim 19 wherein the partial response encoder implements $1 - D^4$.

22. The apparatus of claim 19 wherein the digital interface circuit further comprises a pre-coder to obviate a need for memory in a receiver.

23. The apparatus of claim 19 wherein the wireless interface circuit comprises a global positioning system (GPS) receiver.

24. The apparatus of claim 19 wherein the wireless interface circuit comprises a cellular phone interface.

25. The apparatus of claim 19 wherein the wireless interface circuit comprises a wireless local area network interface.

26. An electronic system comprising:

a first integrated circuit including a wireless interface circuit and a digital data port with a partial response encoder to mitigate interference to the wireless interface circuit;

a second integrated circuit in digital communication with the digital data port of the first integrated circuit; and

an omni-directional antenna coupled to the wireless interface circuit of the first integrated circuit.

27. The electronic system of claim 26 wherein the wireless interface circuit comprises an apparatus to operate between about 800 MHz and about 900 MHz.

28. The electronic system of claim 26 wherein the wireless interface circuit comprises an apparatus to operate between about 2.4 GHz and about 2.5 GHz.

29. The electronic system of claim 26 wherein the partial response encoder includes a filter to implement $1\text{-}D^4$.

30. The electronic system of claim 26 further comprising an adaptive circuit to measure errors in data received by the wireless interface circuit and to modify characteristics of the partial response encoder.